

Harvatek 5.14*3.65*6.95mm OVAL Photo diode

HV-12053T3B

Official Product	HV-12053T3B	Customer Part No.	Data Sheet No.
	*****	*****	CDAE-010-653
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2. A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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Compliance and Certification

ISO9002, QS9000 and ISO14001 Certified

RoHS Compliant



Orderable Information

H V - 12 0 53T 3 B

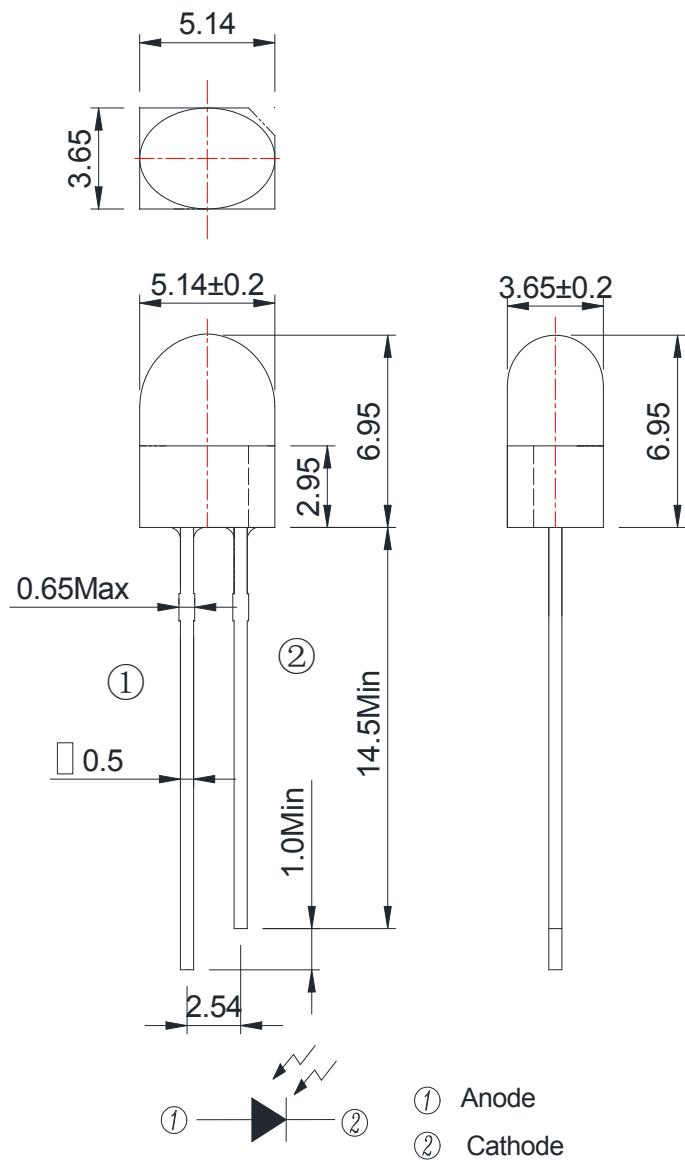


Series Name	Color Code	Remark
HV : HARVATEK	120: 940nm PD Chip. 53T3: 5.14*3.65*6.95mm OVAL LAMP. B : Black Transparent.	

Features:

- Stable Color
- Popular 5.14*3.65*6.95mm through hole package, 6.95mm lens height.
- Black Transparent Lens.

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Package Dimensions:**Notes:**

1. All dimensions are millimeters.
2. Tolerance is ± 0.25 mm unless otherwise noted.
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Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Rating	Unit
Reverse Voltage	V _R	32	V
Operating Temperature	T _{opr}	-25 to +85	°C
Storage Temperature	T _{stg}	-25 to +85	°C
Soldering Temperature*1	T _{sol}	260	°C
Power Dissipation	P _d	150	mW

*1: Soldering time \leq 5 seconds.

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Electrical and Optical Characteristic

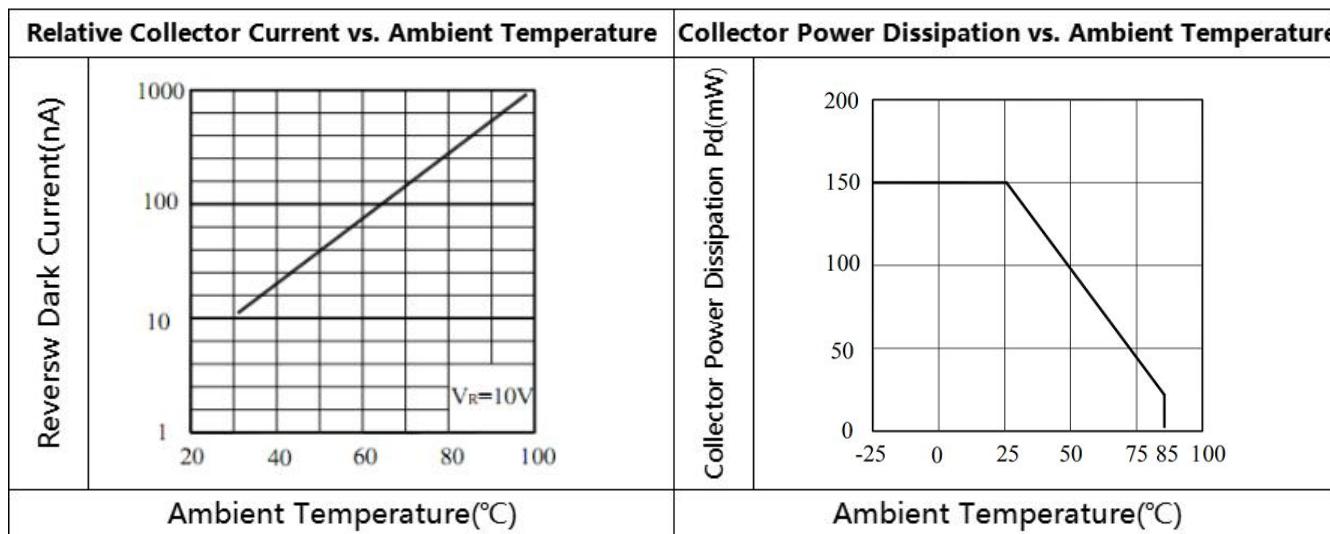
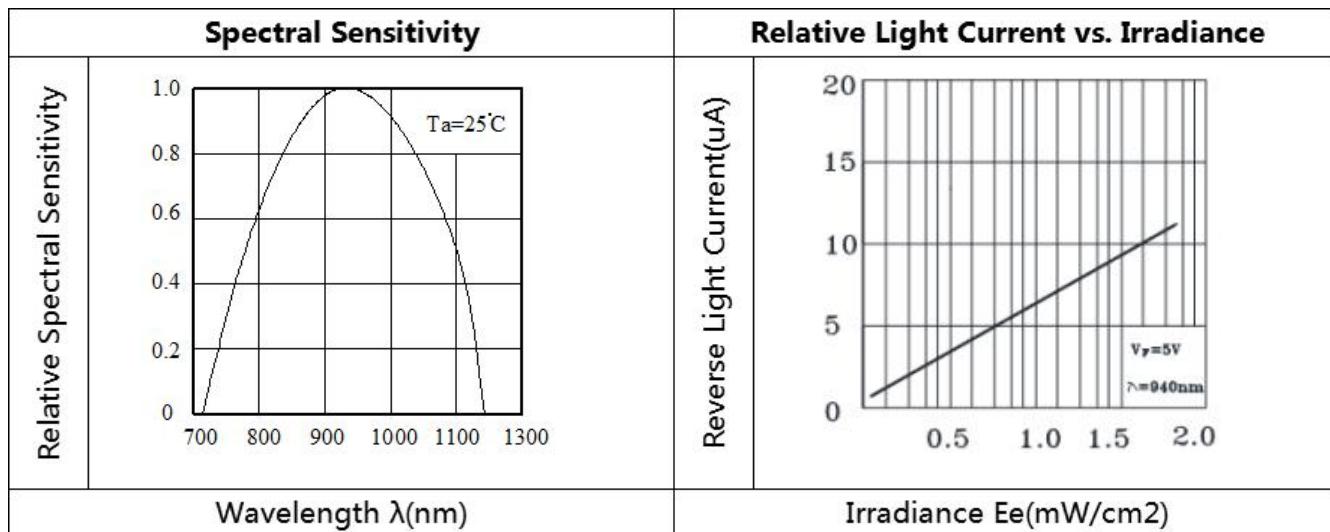
Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Reverse Breakdown Voltage	BV_R	$Ee=0, I_R=100\mu A$	32	/	/	V
Open Circuit Voltage	V_{oc}	$Ee=5\text{mw/cm}^2$ $\Lambda\rho=940\text{nm}$	/	0.44	/	V
Dark Current	I_d	$Ee=0, V_R=10V$	/	/	100	nA
Reverse Light Current	I_L	$Ee=1\text{mw/cm}^2$ $\Lambda\rho=940\text{nm} V_R=5V$	/	4.5	/	uA
Total Capacitance	C_t	$Ee=0$ $V_R=5V f=1\text{mHz}$	/	18	/	pF
Peak Wavelength	$\lambda\rho$	$I_F=20 \text{ mA}$	/	940	/	nm
Rang of Spectral Bandwidth	$\lambda_{0.5}$	$I_F=20 \text{ mA}$	760	/	1100	nm
Rise Time	t_r	$V_{CE}=10V$	/	10	/	nS
Fall Time	t_f		/	10	/	nS

Specifications for Bin Grading:

$I_L (\mu\text{A})$		
Grade	Min.	Max.
10#	2. 96	4. 80
12#	3. 20	6. 84

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Typical Electro-Optical Characteristics Curves



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◆ Reliability test items and conditions:

The reliability of products shall be satisfied with items listed below.

Confidence level: 97%

LTPD:3%

No	Item	Test Conditions	Test Hours/Cycle	Sample Size	Failure Judgment Criteria	Ac/Er
1	Solder Heat	TEMP:260°C±5°C	10 SEC	76 PCS	Iv \leq Ivt*0.5 or Vf \geq U or Vf \leq L	0/1
2	Temperature Cycle	H:+100°C 15min ↓ 5min L:-40°C 15min	300 CYCLES	76 PCS		0/1
3	Thermal Shock	H:+100°C 5min ↓ 10sec L:-10°C 5min	300 CYCLES	76 PCS		0/1
4	High Temperature Storage	TEMP:100°C	1000 HRS	76 PCS		0/1
5	Low Temperature Storage	TEMP:-40°C	1000 HRS	76 PCS		0/1
6	DC Operating Life	TEMP:25°C IF=20mA	1000 HRS	76 PCS		0/1
7	High Temperature / High Humidity	85°C/85%RH	1000 HRS	76 PCS		0/1

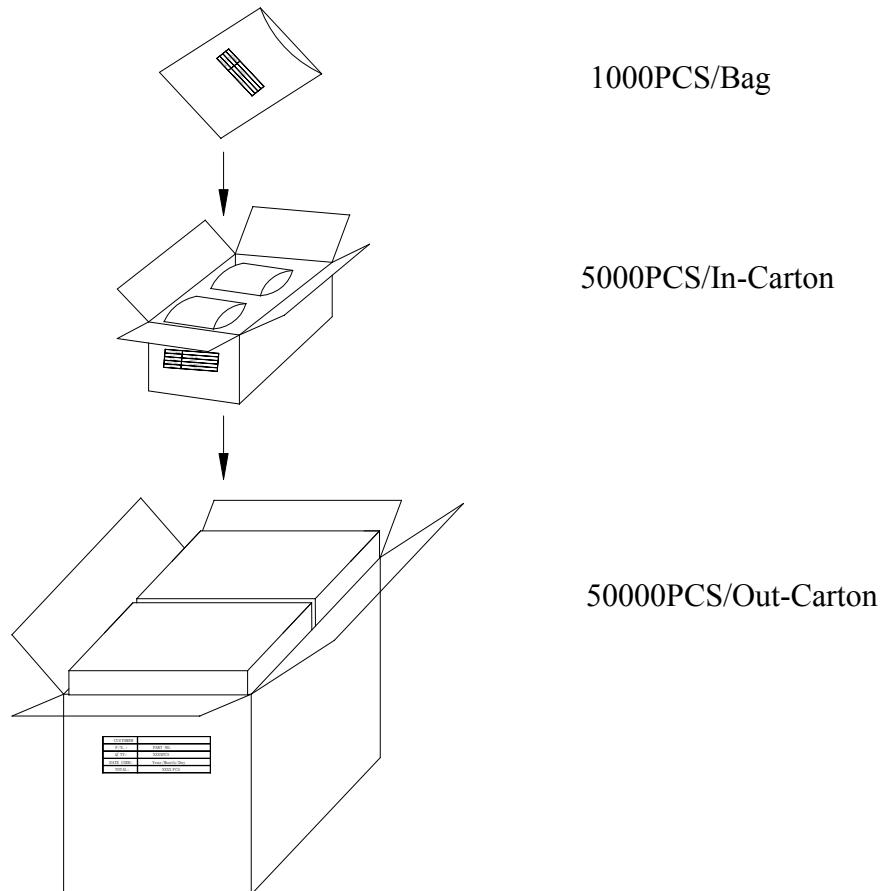
Note: Ivt: To test Iv value of the chip before the reliability test.

Iv: The test value of the chip that has completed the reliability test

U: Upper Specification Limit

L: Lower Specification Limit

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Packing Specification:

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Revision History

Revision	Page	Version No.	Revision Date
Initial Release		1.0	09-11-2019

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